

Claims

[c1] What is claimed is:

1. A power control system used in a computer system, the power control system comprising:
a decision logic for detecting states of the computer system to output a decision voltage;
a voltage control unit for outputting a set voltage according to the decision voltage outputted from the decision logic, the voltage control unit comprising:
a first resistor electrically connected to a voltage source;
a second resistor electrically connected to the first resistor in series connection; and
a switch circuit electrically connected to the first resistor in parallel connection and electrically connected to the decision logic, wherein the switch circuit turns on or turns off according to the decision voltage outputted from the decision logic so that the voltage control unit can output the set voltage; and
a power supply circuit for generating an output voltage for the computer system according to the set voltage.

[c2] 2. The power control system of claim 1 wherein the decision logic comprises:

a south bridge chip for outputting a detecting signal according to states of the computer system;
a voltage converter for converting the detecting signal into a detecting voltage, the voltage converter comprising a resistor and a capacitor; and
a comparator for comparing the detecting voltage with a reference voltage to output the decision voltage.

[c3] 3. The power control system of claim 1 wherein the decision logic comprises a current reader for outputting the decision voltage according to states of the computer system.

[c4] 4. The power control system of claim 1 wherein the decision logic comprises a program code stored in a memory for outputting the decision voltage according to states of the computer system.

[c5] 5. The power control system of claim 1 wherein the switch circuit is a PMOS transistor; when the decision voltage outputted from the decision logic is at a low voltage level, the switch circuit conducts; when the decision voltage outputted from the decision logic is at a high voltage level, the switch circuit turns off.

[c6] 6. The power control system of claim 1 wherein the switch circuit is an NMOS transistor; when the decision

voltage outputted from the decision logic is at a low voltage level, the switch circuit turns off; when the decision voltage outputted from the decision logic is at a high voltage level, the switch circuit conducts.

- [c7] 7. The power control system of claim 1 wherein the voltage control unit further comprises a third resistor electrically connected between the first resistor and the voltage source.
- [c8] 8. The power control system of claim 1 wherein the voltage source is a battery.
- [c9] 9. The power control system of claim 1 wherein the computer system is a notebook or a personal digital assistant (PDA).